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SERIAL NO. 10/696,891

List of Patents and Publications For Applicant's Information Disclosure Statement			1 O <i>i</i>		GRO	GROUP: 2131		
(U:	se severa	al sheets if necessary	437/	TE: October 30, 2003				
S. PATENT DOCUMENTS								
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
		6,473,880	10/29/02	Cypher				
GL		6,453,440	9/17/02	Cypher				
		6,393,597	5/21/02	Cypher				
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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	Al	Dell Computer Corp., David Locklear, "Chipkill Correct Memory Architecture," August 3, 2000, 3 pages.						
GL	A2	Dell, "A White Paper on the Benefits of Chipkill-Correct ECC for PC Server Main Memory," IBM Microelectronics Division, November 1997, pp. 1-23.						
	A3	"The IBM Eserver pSeries 690, Reliability, Availability, Serviceability (RAS)," IBM Technical White Paper, Sept. 2001, 24 pages.						
\mathcal{I}	A4 "Parallel Parity," http://bbs-koi.uniinc.msk.ru/tech1/1994/er_cont/block.htm, 1994, 1 pg.							
7	A5	"Modulo-2 Arithmetic," http://bbs-koi.uniinc.msk.ru/tech1/1994/er_cont/modulo.htm, 1994, 1 pg.						
7	A6	"Introduction to Error Control," http://bbs-koi.uniinc.msk.ru/tech1/1994/er_cont/intro.htm, 1994, 1 pg.						

/Guy Lamarre/ (11/13/2006)

E XAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through Citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.